## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: T. MIYAMOTO et al

Serial No. 10/046,446
Filed: January 16, 2002
Group Art Unit: 2811
Examiner: D. Owens

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD OF THAT

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents Alexandria, VA 22313

Sir:

In accordance with the duty of disclosure, the applicants inform the Examiner of the documents cited during prosecution of the parent application, USSN 10/046,446.

The above-referenced application is a divisional application of U.S. Application Serial No. 10/046,446, filed January 16, 2002.

The Applicant requests the Examiner to initial and return a copy of the attached PTO-1449 form as an indication that the references have been considered.

Respectfully submitted,

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Date: February 5, 2004

U.S. DEPARTMENT OF COMMERCE ATTY. DOCKET NO. FORM PTO-1449 SERIAL NO. PATENT AND TRADEMARK OFFICE T&A-110-02 (REV. 7-80) APPLICANT T. MIYAMOTO et al LIST OF DOCUMENTS CITED BY APPLICANT (Use several sheets if necessary) FILING DATE **GROUP** February 5, 2004 **U.S. PATENT DOCUMENTS** FILING DATE **EXAMINER** DOCUMENT DATE NAME CLASS SUBCLASS INITIAL (If Appropriate) AA 2002/ 06/2002 Lee, Joo-hern 0076913 AB 2002/ 09/2002 Jiang et al 0125568 AC 6,469,393 10/2002 Oya, Y. AD 2003/ 08/2003 Jiang et al 0153099 08/2000 AE 6,103,552 Lin, Mou-Shiung AF 6,621,164 09/2003 Hwang et al AG ΑH ΑI ΑJ ΑK **FOREIGN PATENT DOCUMENTS** TRANSLATION CLASS **SUBCLASS** DOCUMENT DATE COUNTRY YES NO WO99/ 5/14/99 WIPO AL 23696 7-161761 6/23/95 Japan AM 11-40563 2/12/99 Japan ΑO AΡ OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.) "Electronics Mount Technology Extra Edition 2000", Gijyutsu-chyuosa-kai AR Corp., issued May 28, 2000, pp. 81-113. AS ΑT